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## Atomic force microscope cantilevers for combined thermomechanical data writing and reading

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Heat conduction governs the ultimate writing and reading capabilities of a thermomechanical data storage device. This work investigates transient heat conduction in a resistively heated atomic force microscope cantilever through measurement and simulation of cantilever thermal and electrical behavior. The time required to heat a single cantilever to bit-writing temperature is near 1  $\mu$ s and the thermal data reading sensitivity  $\Delta R/R$  is near  $1 \times 10^{-4}$  per vertical nm. Finite-difference thermal and electrical simulation results compare well with electrical measurements during writing and reading, indicating design tradeoffs in power requirements, data writing speed, and data reading sensitivity. We present a design for a proposed cantilever that is predicted to be faster and more sensitive than the present cantilever. © 2001 American Institute of Physics.

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In current magnetic data storage, the superparamagnetic effect limits magnetic domain stability and will ultimately limit room-temperature magnetic data density near 100 Gb/in<sup>2</sup>. <sup>1</sup> Several promising alternative data storage technologies that could surpass this limit involve the use of scanningprobe devices.<sup>2</sup> Thermomechanical data storage is one such technology,<sup>3–5</sup> demonstrating data density as high as 400 Gb/in<sup>2.5</sup> In thermomechanical data storage, a resistively heated atomic force microscope (AFM) cantilever writes a data bit by scanning over a polymer substrate. Heat and force applied by the cantilever tip to the polymer cause it to soften and flow, writing a data bit, as shown in Fig. 1.<sup>3-5</sup> The same cantilever can be used to detect the presence of a previously written data bit.5 As the cantilever tip follows the contour of a data bit, the reduction in thermal impedance between the cantilever and the substrate causes a lower temperature rise in the heated cantilever, also shown in Fig. 1.

Previous research on thermomechanical data storage has focused on improving both data density and data rate. Silicon cantilevers with small solid-state resistive heaters near the cantilever tip allowed reduction of the cantilever heating time.<sup>4</sup> Array operation of heater cantilevers allows higher data rates than a single cantilever configuration. We have recently fabricated a functional array of 32×32 heater cantilevers<sup>6,7</sup> and report parallel writing and reading operation of this array in Ref. 7. Binnig *et al.*<sup>5</sup> demonstrated a data density of 400 Gb/in.<sup>2</sup> by writing thermomechanical data bits of diameter 40 nm in a poly-methylmethacrylate (PMMA) layer of thickness 40 nm. The very thin polymer layer confines the polymer melted region to dimensions on the order of the polymer thickness, and limits tip penetration and therefore data bit size. Recent research investigated heat

transfer during thermal data writing and reading. King and co-workers<sup>8</sup> showed that the temperature of the tip-polymer interface is significantly higher than the temperature induced in the substrate due to the presence of the heater cantilever alone. These authors also showed that only a fraction of the total heat is transferred through the tip, and that much more of the heat passes across the cantilever-sample air gap,<sup>9</sup> concluding that the presence of the heated tip permits data writing and that the presence of the heater cantilever permits data reading. Simultaneous improvement of cantilever writing and reading figures of merit is therefore possible,<sup>10</sup> given



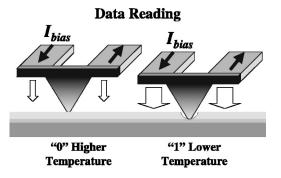


FIG. 1. Thermomechanical data bit writing and thermal data reading with a resistively heated AFM cantilever.

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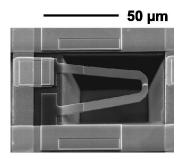


FIG. 2. Scanning electron microscopy image of the measurement cantilever.

detailed measurement and modeling of cantilever behavior.

This letter reports measurement and simulation of single-cantilever operation during data bit writing and thermal data reading. An electrical and thermal simulation predicts operation of a single cantilever identical in design to the array cantilever of Despont et al.<sup>6</sup> and is verified by electrical measurements. Figure 2 shows a scanning electron micrograph of the present cantilever. The simulation is then used to analyze a cantilever of a proposed new design. Table I lists the design attributes of both cantilevers. The proposed cantilever is designed with a smaller heater region to reduce the heating time<sup>5</sup> and has a shorter tip to increase the data reading sensitivity. At 200 nm thickness, the present cantilever is the thinnest we have fabricated to date, for maximum thermal data reading sensitivity.9 The remaining cantilever dimensions are selected so as to achieve a 50% improvement in the cantilever mechanical resonant frequency. The cantilever mechanical resonant frequency will approximately govern the shortest time between repeated bit writing and reading events (see, for example, Ref. 11).

The writing measurement is made by placing the cantilever in series with a sense resistor of 3.3 k $\Omega$ , and square voltage pulses of duration 1-25  $\mu$ s and pulse amplitude of 1-15 V drive the circuit. The measurement of the voltage across the circuit and the sense resistor allows the calculation of cantilever resistance. Data reading sensitivity measurements are made by slowly moving the heated cantilever into contact with the polymer surface.

A finite-difference simulation calculates electrical heat generation in the cantilever and heat transfer along the cantilever and into the nearby air, with a spatial resolution of 50 nm and an explicit time advancement of 1 ns. Circuit design models<sup>12</sup> calculate the temperature-dependent intrinsic carrier generation and the temperature-dependent electrical resistivity of the doped silicon. The thermal conductivity of the highly doped silicon cantilever is assumed to be 50 Wm<sup>-1</sup> K<sup>-1</sup> at room temperature<sup>13</sup> and vary as the inverse of absolute temperature. For each simulation time advance-

TABLE I. Design details for present and proposed cantilevers.

	Present cantilever	Proposed cantilever
Heater area	5 μm×7μm	2 μm×5 μm
Thickness	200 nm	200 nm
Tip height	500 nm	200 nm
Leg width	$10 \mu m$	$5 \mu m$
Leg length	50 μm	$20~\mu\mathrm{m}$
Electrical resistance at 25 °C	$2120 \Omega$	2012 Ω
T	220 177	220 1 11

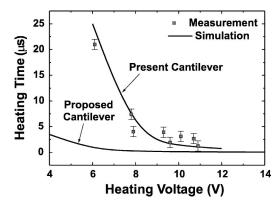


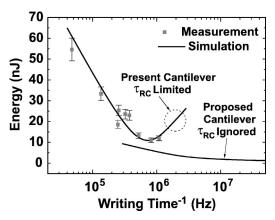
FIG. 3. Measurement and simulation of the time required to reach the bitwriting temperature as a function of voltage applied to the cantilever.

ment, the simulation calculates the total cantilever electrical resistance, which determines the cantilever current at fixed driving voltage. The cantilever current determines the heating power at each position along the length of the cantilever. The simulation agrees with analytical solutions to within 2%, and further variation between measurement and simulation is due to noise and parasitic capacitance in the cantilever.

Figure 3 shows measurement and prediction of the time required for the cantilever to reach the writing temperature. The thermomechanical writing temperature is taken to be 350 °C, which is the measured writing temperature in thin PMMA.<sup>5,7</sup> Predictions indicate that the lowest applied voltage at which the present cantilever can write data is near 6 V.

Figure 4 shows measurement and prediction of the energy required for a cantilever to write a single data bit. The diffusion of heat is confined to a smaller region in the cantilever for shorter heating pulses of higher voltage, which corresponds to the lower energy operation. The design of a thermomechanical data storage system must balance the requirements of low power and voltage with the requirements of low energy consumption.

Figure 5 shows measurement and prediction of the thermal data reading sensitivity. Higher heating power corresponds to a greater change in the fraction of the heat that is transferred across the cantilever-substrate air gap for a given change in gap height. The cantilever temperature should not be high enough to deform the polymer during reading. The nonlinearity in the curve is due in part to the change in cantilever electrical resistance with temperature. Tips much



This articl Resonant frequency, indicated in the 220 kHz. Reuse of A330 kHzent is sub FIG. 4. Measurement and simulation of the energy required for a single to provide the control of the energy required to the energy required to the control of the energy required to the control of the energy required to the energy required cantilever to write a data bit as a function of heating time.

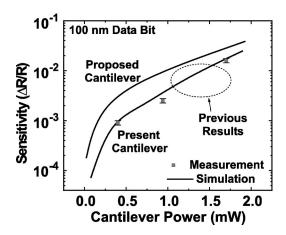


FIG. 5. Measurement and prediction of thermal data reading calculated for reading a data bit of depth 100 nm. Previous data-reading sensitivity measurements are also indicated (Refs. 5 and 7).

shorter than 200 nm will probably not improve reading sensitivity significantly as air rarefaction becomes important.

Predictions for the operation of the proposed cantilever indicate that this improved design offers important reductions of the heating time, power and energy requirements, and improves reading sensitivity. In particular, the lowest voltage at which a data bit could be written is nearly halved by the proposed cantilever, and the heating time is also reduced.

This work analyzes the impact of heat conduction in a resistively heated cantilever during thermomechanical data writing and reading and presents comprehensive thermal design of cantilevers for thermomechanical data storage. It is possible to improve at the same time both the heating characteristics that govern data bit writing and the sensitivity governing data bit reading. Although the proposed cantilever could be fabricated with no changes in the fabrication process, we envision forefront fabrication technology to realize the ultimate limits of speed, power, and sensitivity in thermomechanical data storage. The ultimate limits of thermomechanical data storage will further depend upon electronics integration and engineering of the polymer data layer.

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